

The 2014 International Conference on Integrated Circuits, Design, and Verification (ICDV 2014) PROGRAM

Address: Conference room #3, Building E3, VNU University of Engineering and Technology
144 Xuan Thuy road, Cau Giay district, Hanoi, Vietnam

14-Nov-2014					
START	STOP	Type	Authors	Affiliation	Title
8:00	8:30		REGISTRATION		
Session #1		Location: 3-G3	Chairs: Toshimasa Matsuoka, Osaka University Ngoc Binh Nguyen, The Radio Eletronics Association of Vietnam (REV)		
8:30	8:50	Opening	- Prof. Ngoc Binh Nguyen, President of the Radio Eletronics Association of Vietnam (REV), IEICE Vietnam Section Chair - Prof. Nguyen Thanh Thuy, Vice Rector of VNU University of Engineering and Technology - Dr. Takeshi Yamamura, IEICE ICD Chair - Dr. Toshiyuki Shibuya, IEICE VLD Chair		- Welcome speech - Opening speeches
8:50	9:50	Keynote	Diego Puschini	CEA-LETI, MINATEC, France	Power Management in UTBB FD-SOI Circuits
9:50	10:10	Break	Photo session		
Session #2		Location: 3-G3	Chairs: Xuan-Tu Tran, VNU University of Engineering and Technology Cong Kha Pham, The University of Electro-Communications		
10:10	10:40	Invited	Koichiro Ishibashi, Cong-Kha Pham, Nobuyuki Sugii	The University of Electro-Communications, Japan Low-power Electronics Association & Project (LEAP), Japan	Perpetuum-Mobile Sensor Network Systems using a CPU on 65nm SOTB CMOS Technology
10:40	11:00	Oral	Thanh-Tri Vo, Tran-Son Nguyen, Trong-Tu Bui, Duc-Hung Le	The University of Science - VNUHCM, Vietnam The University of Information Technology - VNUHCM, Vietnam	Optimization of BSIM3v3 Model Parameters Using Lavenberg-Marquardt Algorithm
11:00	11:20	Oral	Minh-Thien Hoang, Nobuyuki Sugii, Koichiro Ishibashi	The University of Electro-Communications, Japan	A 0.75V 0.574mW 2.16GHz - 3.2GHz Differential Multi-pass Ring Oscillator on 65nm SOTB CMOS Technology
11:20	11:40	Oral	Huyen Thanh Pham, Thang Vu Nguyen, Hideyuki Murata, Toan Thanh Dao	University of Transport and Communications, Vietnam Hanoi University of Science and Technology, Vietnam	Computer-Aided Design and Verification of Organic CMOS Integrated Circuits for Bioelectronics
11:40	12:00	Oral	Takumu Yomogita, Cong-Kha Pham	The University of Electro-Communications, Japan	A 0.9V 200kHz Current-mode Successive Approximation Analog-to-Digital Converter in 0.18 μ m CMOS Technology
12:00	13:00	LUNCH			
Session #3		Location: 3-G3	Chairs: Trong-Tu Bui, HCM University of Science		
13:00	13:30	Invited	Koji KOTANI	Tohoku University, Japan	Synergistic Energy Harvesting from Radio Waves by Photovoltaic-Assisted CMOS Rectifier
13:30	13:50	Oral	Satoshi Tanabe, Atsuchi Muramatsu, Ken-Ichi Kawasaki, Makoto Mori, Teruo Ishihara	Fujitsu Laboratories, Japan	Reducing Energy Consumption of Low-power SoCs by 45.5% by Applying DVFS and a Multi-level-shift Architecture
13:50	14:10	Oral	Pham Van Huong, Nguyen Ngoc Binh, Vu Van Phuc	VNU University of Engineering and Technology, Vietnam	A New Approach to Optimizing the Power Consumption of Existed Embedded Systems Based on the Combination of Hardware and Software
14:10	14:30	Oral	Sou Tashino, Jiro Ida	Kanazawa Institute of Technology, Japan	Scaling of Substrate Bias Sensitivity of MOSFET and its Impact on Circuit Operation for Ultra-Low Power Application
14:30	15:30	Break & Poster			
Session #4		Location: 3-G3	Chairs: Van-Phuc Hoang, Le Quy Don Technical University		
15:30	16:00	Invited	Eishi Ibe, Tadanobu Toba, Ken-ichi Shimbo, Takumi Uezono	Hitachi, Japan	Challenges beyond 2010s against Terrestrial Radiation-Induced Failures in Electronic Systems
16:00	16:20	Oral	Van-Lan Dao, Van-Phuc Hoang	Le Quy Don Technical University, Vietnam	Memory Usage and Performance Analysis of AES Cryptography Implementation on 8-bit MCUs
16:20	16:40	Oral	Tien-Luan Vu, Van-Quy Quach, Duy-Hieu Bui, Xuan-Tu Tran	VNU University of Engineering and Technology, Vietnam	A low-cost implementation of Advance Encryption Standard
16:40	17:00	Oral	Van Toan Nguyen, Huu Thuan Huynh, Cong Kha Pham	HCM University of Science, Vietnam The University Electro-Communications, Japan	Designing a High Performance Cryptographic System for Video Applications
17:00	17:20	Oral	Khac-Hoang Ngo, Quoc-Tuan Nguyen	VNU University of Engineering and Technology, Vietnam	Implementation of Network Coding Scheme in Universal Software Radio Peripheral
17:20	20:20	Banquet	The banquet will start at 18h15		

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Session #5					
Location: 3-G3		Chairs: Van-Phuc Hoang, Le Quy Don Technical University			
8:30	9:00	Invited	Kiichi NIITSU, Kazuo NAKAZATO,	Nagoya University, Japan	CMOS Platforms for Biomedical Application -- DNA Sequencing, Glucose Detection, and Bacteria Counting
9:00	9:30	Invited	Takahiro Miki	Renesas Electronics, Japan	A/D Converter Trends and Topics in ISSCC 2014
9:30	9:50	Oral	Hung K. Nguyen, Quang-Vinh Tran, Xuan-Tu Tran	VNU University of Engineering and Technology, Vietnam	Data Locality Exploitation for Coarse-grained Reconfigurable Architecture in Reconfigurable Network-on-Chips
9:50	10:10	Oral	Dang Trong Trinh, Nguyen Quang Vinh	RMIT University Vietnam	Enhanced hardware implementation of CDF 9/7 Discrete Wavelet Transform with symmetric extension in JPEG2000 system
10:10	10:30	Oral	Trong-Uoc Bui, Ba-Duy Dao, Huu-Hai Nguyen	VNU University of Engineering and Technology, Vietnam	Design and Implementation of a Dual-Mode (Gray & Color) Image Encoding System
10:30	10:50	Break & Poster			
Session #6					
Location: 3-G3		Chairs: Truong Vu Bang Giang, VNU University of Engineering and Technology			
10:50	11:20	Invited	Masaya Miyahara, Ibuki Mano, Masaaki Nakayama, Kenichi Okada, Akira Matsuzawa	Tokyo Institute of Technology, Japan	Analog to Digital Converters for Milli-meter Wave Wireless Communications
11:20	11:50	Invited	Yasufumi Sakai, Takayuki Shibasaki, Hisakatsu Yamaguchi, Toshihiko Mori, Yoichi Koyanagi,, Hiroataka Tamura,	Fujitsu Laboratories Ltd., Japan	A 56-Gb/s Receiver Front-End with a CTLE and 1-Tap DFE in 20-nm CMOS
11:50	12:10	Oral	Le Trong Trung, Nguyen Quoc Dinh	Le Quy Don Technical University, Vietnam	Proposal of a compact MIMO Ultra-Wide Band Antenna and Low Mutual Coupling
12:10	13:10	Lunch			
Session #7					
Location: C31		Chairs: Nguyen Quoc Dinh, Le Quy Don Technical University			
13:10	13:40	Invited	Tatsuru MATSUO	Fujitsu Laboratories Ltd., Japan	Advanced techniques for E-Fuse application
13:40	14:00	Oral	Ngoc-Anh Vu, Van-Phuc Hoang	Center of High Technology Communications, VietNam Le Quy Don Technical University, Vietnam	An All Digital HF Transceiver on FPGA
14:00	14:20	Oral	Nguyen Quoc Duy, Nguyen Minh Tran, Truong Vu Bang Giang	VNU University of Engineering and Technology, Vietnam	A Wideband Microstrip Antenna for IEEE 802.11ac Indoor Applications
14:20	14:40	Oral	Doan Huu Chuc , Bach Gia Duong	Hai Phong Private University, Vietnam VNU University of Engineering and Technology, Vietnam	Study, design and fabrication of rectifying antenna circuit topologies for microwave power transmission system operating at S band
14:40	15:00	Break			
Session #8					
Location: 3-G3		Chairs: Pham Nguyen Thanh Loan, Hanoi University of Science and Technology			
15:00	15:30	Invited	Atsushi TAKAHASHI, Ahmed AWAD, Yukihide KOHIRA, Tomomi MATSUI, Chikaaki KODAMA, Shigeki NOJIMA, Satoshi TANAKA	Tokyo Institute of Technology, Japan The University of Aizu, Japan Toshiba Corporation, Japan	Multi Patterning Techniques for Manufacturability Enhancement in Optical Lithography
15:30	15:50	Oral	Hao Nguyen-Van, Cuong Dao, Long Nguyen, Minh Nguyen, Loan Pham-Nguyen	Hanoi University of Science and Technology, Vietnam	A Fast and Long-Life Li-Ion Battery Charger With Decoupled Current-Source
15:50	16:10	Oral	Tuan Anh Vu, Duong Bach Gia, Tor Sverre "Bassen" Lande	University of Oslo, Norway VNU University of Engineering and Technology, Vietnam	A 3–5 GHz Noise-Cancelling CMOS Low Noise Amplifier for IR-UWB Receivers
16:10	16:30	Oral	Tuan Anh Vu, Duong Bach Gia, Tor Sverre "Bassen" Lande	University of Oslo, Norway VNU University of Engineering and Technology, Vietnam	A 1–6 GHz Downconversion Quadrature CMOS Mixer for IR-UWB Receivers
16:30	16:50	Oral	Dinh-Thien Vu, Trong-Thuc Hoang, Ngoc-Hung Nguyen, Trong-Tu Bui	HCM University of Science, Vietnam	An FPGA-based Variable-length 4-Channel Separation FastICA Implementation
16:50	17:00	Closing	- Prof. Cong-Kha Pham* - Prof. Ngoc Binh Nguyen**	*UEC, Japan **IEICE Vietnam Section, VNU-UET	- Conference summary - Closing speech & ICDV 2014 announcement

POSTER PRESENTATIONS					
FIRST DAY POSTER SESSION	14h30 - 15h30 14-Nov- 2014	Poster	Thai Mai Dinh Thi, Quoc-Tuan Nguyen, Khac-Hoang Ngo	VNU University of Engineering and Technology, Vietnam	Implementation of Spectrum Sensing Scheme in Software- Defined Radio Testbed
		Poster	Dai Van Nguyen, Toan Thanh Dao	University of Transport and Communications, Vietnam	Operational Analysis of Nanoscale ReRAM Cell with MATLAB
		Poster	Nguyen Minh Tran, Truong Vu Bang Giang	VNU University of Engineering and Technology, Vietnam	Design and Fabrication of an antenna for Global Navigation Satellite System Applications
		Poster	Van-Tai Tran, Van-Huy Nguyen, Trinh Anh Vu , Thang X. Vu	VNU University of Engineering and Technology, Vietnam Singapore University of Technology and Design, Singapore	Design and Implementation of the Alamouti-OFDM system based on FPGA
		Poster	Giang Bach Hoang, The Anh Nguyen Dinh, Tuan Anh Vu, Duong Bach Gia	VNU University of Engineering and Technology, Vietnam University of Oslo, Norway	Research, Design and Fabrication of a 2.4 GHz 130 W Power Amplifier Module for Free-Space Energy- Transmission Systems
		Poster	Nguyen Phuoc Thong, Nguyen Van Cuong	Danang University of Technology, Vietnam	FPGA-Based Implementation of the Fire Alarm Center in Wireless Sensor Network for High Buildings
		Poster	Dang Trong Trinh, Nguyen Minh Chau	RMIT University Vietnam	A Compact Functional Model of DDR4 SDRAM Model for Future System-on-Chip Verification Environment
		Poster	Binh-Son Le, Vy-Long Dang, Trong- Tu Bui	HCM University of Science, Vietnam	Swarm Robotics Simulation Using Unity