

**The 2012 International Conference on Integrated Circuits and Devices in Vietnam (ICDV 2012)**  
**PROGRAM**

*Address: Learning Resource Center, Danang University of Technology (DUT)  
 54 Nguyen Luong Bang street, Lien Chieu district, Danang, Vietnam*

13-Aug-2012								
START	STOP	TIME	submission#	Type	Authors	Affiliation	Country	Title
8:30	9:30	60			<b>REGISTRATION</b>			
9:30	9:50	20		<b>Opening</b>	- Prof. LE Kim Hung* - Prof. NGUYEN Ngoc Binh** - Prof. Masahiko YOSHIMOTO** - Prof. Toshimasa MATSUOKA***	* DUT, Vietnam * VNU-UET, Vietnam **Kobe University, Japan ***Osaka University, Japan (TPC co-chair)	Vietnam Japan	- Welcome speech - Opening speech 1 (Vietnam side) - Opening speech 2 (Japan side) - Introduction to ICDV 2012 program
<b>Session #1</b>		<b>Location: Song Han room</b>			<b>Chairs: Ngoc-Binh Nguyen (VNU University of Engineering and Technology, Vietnam) Masahiko Yoshimoto (Kobe University, Japan)</b>			
9:50	10:50	60	106	<b>Keynote</b>	Toru Shimizu*, Yohei Sato*, Hiroshi Ueki*, Shintaro Mori*, Hiroyuki Kawai*, Masanori Hayashikoshi*, Hiroshi Nakamura**	*Renesas **University of Tokyo	Japan	Challenge for the Normally-off Computing
10:50	11:30	40	100	<b>Invited</b>	Yutaka Uematsu, Masayoshi Yagyu, and Hideki Osaka	Yokohama Research Lab., Hitachi, Ltd., Japan	Japan	Power Integrity Design For Low Power Supply Voltage Applications
11:30	11:50	20	9	Oral	Kazuyuki Momotani, Keishi Sakanushi, Yoshinori Takeuchi, Masaharu Imai	Osaka University, Japan	Japan	Low Energy Application Specific Instruction set Processor for Scramble Processing
<b>11:50</b>	<b>13:00</b>	<b>70</b>		<b>LUNCH</b>	<b>Buffet lunch</b>			

Session #2		Location: Song Han room			Chairs: Minoru Fujishima (Hiroshima University, Japan) Truong Vu Bang Giang (VNU University of Engineering and Technology, Vietnam)			
13:00	13:40	40	101	TJMW-special talk	Takashi Ohira	Toyohashi University of Technology, Japan	Japan	Seven Physical Meanings of the Quality Factor in Resonators and Oscillators
13:40	14:20	40	102	Invited	Ramesh K. Pokharel, R. Dong, and K. Yoshida	Kyushu University, Japan	Japan	Design of Low-loss on-chip passive devices in CMOS technology for 60 GHz band and Status of LSI Education in E-JUST, a Japanese-Style University in Egypt
14:20	14:40	20	36	Oral	Long Nguyen, Chien Le, Toan Nguyen, Hao Pham, Chien Dao-Ngoc, Loan Pham-Nguyen	Hanoi University of Science and Technology, Vietnam	Vietnam	A 0.13um Low Power LNA 5Ghz Using Cascode Topology for WSN Applications
14:40	15:00	20	13	Oral	M.A. Othman* **, I. Harrison*, M.M. Ismail**, H.A.Sulaiman**, M.H.Misran**	* University of Nottingham, United Kingdom **Universiti Teknikal Malaysia Melaka, Malaysia	United Kingdom Malaysia	Sub-Terahertz Detection using 250 nm MOSFET's gate length: Power Sensitivity and Frequency Response
15:00	15:20	20		Break				
Session #3		Location: Song Han room			Chairs: Anh-Vu Dinh-Duc (HCM University of Technology, Vietnam) Toshihiko Hamasaki (Hiroshima Institute of Technology, Japan)			
15:20	16:00	40	108	Invited	Derek Jao	Mentor Graphics	USA	Leader in Functional Verification Solution – Mentor Graphics
16:00	16:20	20	19	Oral	Anh-Vu Dinh-Duc	HCMC University of Technology, Vietnam	Vietnam	A Novel ZigBee Network Layer Architecture for TinyOS 2.x
16:20	16:40	20	7	Oral	Xin Zhang*, Yu Pu*, Koichi Ishida*, Yoshikatsu Ryu**, Yasuyuki Okuma**, Po-Hung Chen*, Kazunori Watanabe**, Takayasu Sakurai*, Makoto Takamiya*	*University of Tokyo, Japan **STARC, Japan	Japan	Low Voltage Switched-Capacitor Voltage Converter with Improved Light Load Efficiency
16:40	17:00	20	26	Oral	Pham Van Huong, Nguyen Ngoc Binh, Bui Ngoc Hai, Vu Van Phuc	VNU University of Engineering and Technology, Vietnam	Vietnam	Hardware-Software Co-Design to Optimize Embedded System by Pareto Principle and DSL
17:00	20:00	180	-	Banquet	Bus leaves DUT at 17h15 and will be at the restaurant at 17h45. The banquet will start at 18h00			

**14-Aug-2011**

Session #4		Location: Song Han room			Chairs: Toshimasa Matsuoka (Osaka University, Japan) Nguyen Van Cuong (Danang University of Technology, Vietnam)			
8:30	9:30	60	103	Keynote	Toshihiko Hamasaki	Hiroshima Institute of Technology, Japan	Japan	Challenge of 24bit-class Analog in Audio DAC
9:30	9:50	20	14	Oral	Trung-Khanh Le*, Trong-Tu Bui*, Duc-Hung Le**, Cong-Kha Pham**	*The University of Science, Ho Chi Minh City, Vietnam **The University of Electro-Communications, Tokyo, Japan	Vietnam Japan	A Design of 16-bit Pi-Type DAC Employing Three-Stage Indirect Feedback Compensation Opamp
9:50	10:10	20	17	Oral	Xuan-Vy Luu*, Xuan-Thuan Nguyen*, Duc-Hung Le**, Trong-Tu Bui*	* The University of Science, Ho Chi Minh City, Vietnam **The University of Electro-Communications, Tokyo, Japan	Vietnam Japan	An ASIC-Oriented Design of High Performance Decimation Filter for Oversampling A/D Converters
10:10	10:30	20		Break				
Session #5		Location: Song Han room			Chairs: Ramesh K. Pokharel (Kyushu University, Japan) Truong Vu Bang Giang (VNU University of Engineering and Technology, Vietnam)			
10:30	11:10	40	105	Invited	Masaya Kibune, Hirotaka Tamura	Fujitsu Laboratories Ltd., Japan	Japan	Analog Front-End Design for CMOS High-Speed I/O
11:10	11:30	20	28	Oral	M.A.Othman, Z.A.F.M.Napiah, N.Y.M.Yasin, T.S.M.Arshad, M.M.Ismail, H.A.Sulaiman, M.H.Misran, M.A.M.Said	Universiti Teknikal Malaysia Melaka, Malaysia	Malaysia	An Investigation on I-V Characteristic for CMOS PIN Photodiode: Variable I-Layer
11:30	11:50	20	30	Oral	M.A.Othman, Z.A.F.M.Napiah, T.S.M.Arshad, N.Y.M.Yasin, M.M.Ismail, H.A.Sulaiman, M.H.Misran, M.A.M.Said	Universiti Teknikal Malaysia Melaka, Malaysia	Malaysia	I-V Characteristic at Different Depletion Region for CMOS PN Photodiode for Optical Communication Applications
11:50	12:10	20	33	Oral	Long Hoang Duc, Duong Bach Gia	VNU University of Engineering and Technology, Vietnam	Vietnam	Study, Design, and Fabrication of Solid-State Transmitter to be used for the Wide-Band Radar System Operating at UHF Band (820MHz-900MHz)
12:10	13:10	60		Lunch				

<b>Session #6</b>		<b>Location: Song Han room</b>			<b>Chairs: Xuan-Tu Tran (VNU University of Engineering and Technology, Vietnam) Quang-Vinh Tran (VNU University of Engineering and Technology, Vietnam)</b>			
13:10	13:50	40	107	<b>Invited</b>	Ellen Tam*, Khoa Nguyen**	*Cadence **AMD	USA	Education and Learning
13:50	14:10	20	92	Oral	Duy-Hieu Bui*, Van-Huan Tran*, Van-Mien Nguyen*, Duc-Hoang Ngo**, Xuan-Tu Tran*	*VNU University of Engineering and Technology, Vietnam **ICDREC, VNU-HCMC, Vietnam	Vietnam	A Hardware Architecture for Intra Prediction in H.264/AVC Encoder
14:10	14:30	20	40	Oral	Trong-Tuan Nguyen*, Van-Cuong Nguyen**, Hung-Manh Pham**	* Acronics Systems company, Inc **Danang University of Technology, Vietnam	Vietnam	Enhance the Performance and the Security of SoC using Pipeline and dynamic partial reconfiguration
14:30	14:50	20	12	Oral	Kazuya Yokohari, Koyo Nitta, Mitsuo Ikeda, Atsushi Shimizu	NTT Media Intelligence Laboratories, Japan	Japan	Design Methodology for Video Codec LSI with High-level Synthesis and FPGA-based Platform
14:50	15:10	20	-	<b>Break</b>				
<b>Session #7</b>		<b>Location: Song Han room</b>			<b>Chairs: Masahiko Yoshimoto (Kobe University, Japan) Ngoc-Binh Nguyen (VNU University of Engineering and Technology, Vietnam)</b>			
15:10	16:40	90	-	<b>Panel Session</b>	<b>PANEL SESSION: IC Design Development and The Future for Vietnam</b>			

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Session #8		Location: Song Han room			Chairs: Takeshi Yamamura (Fujitsu Laboratories, Japan) Bui Trong Tu (HCM University of Science, Vietnam)			
8:30	9:10	40	104	Invited	Yoshinori Takeuchi, Taichiro Shiraishi, Keishi Sakanushi, Masaharu Imai	Osaka University, Japan	Japan	Task Assignment Methods for Multi-Processor System considering Dynamic Voltage Frequency Scaling
9:10	9:30	20	31	Oral	Minh D. Nguyen*, ChungV. Nguyen*, Wolfgang Kunz**	*Hanoi University of Science and Technology, Vietnam **University of Kaiserslautern, Germany	Vietnam Germany	DDR2 Formal Compliance Verification
9:30	9:50	20	18	Oral	Xuan-Thuan Nguyen*, QM-Dang-Do*, Hoang-Dat Tran*, Huu-Thuan Huynh*, Cong-Kha Pham**	*The University of Science, Ho Chi Minh City, Vietnam **The University of Electro-Communications, Tokyo, Japan	Vietnam Japan	A PCIe-based FFT Implementation for High-speed Spectrum Analysis
9:50	10:10	20	91	Oral	Thanh-Vu Le-Van, Dien-Tap Ngo, Xuan-Tu Tran	VNU University of Engineering and Technology, Vietnam	Vietnam	A SystemC based Simulation Platform for Network-on-Chip Architectures
10:10	10:30	20		Break				
Session #9		Location: Song Han room			Chairs: Cong-Kha Pham (Electro-Communications University, Japan) Xuan-Tu Tran (VNU University of Engineering and Technology)			
10:30	10:50	20	10	Oral	Ryouta Shirono*, Vu Trong Thien**, Kohichiro Ishibashi*	*The University of Electro-communications, Chofu, Japan **Ho Chi Minh City University of Technology, Vietnam	Japan Vietnam	Sleep Mode Implementation to ZigBee Router Devices for Wireless Sensor Networks
10:50	11:10	20	11	Oral	Phuong-Phuong Tang, Xuan-Thuan Nguyen, Huu-Thuan Huynh	University of Science, Ho Chi Minh City, Vietnam	Vietnam	A SoPC for High-Speed Low-Resource Motion JPEG Encoder
11:10	11:30	20	27	Oral	Pham Van Huong, Nguyen Ngoc Binh, Nguyen Thu Huyen, Nguyen Thuy Duong, Tran Nghi Phu	VNU University of Engineering and Technology, Vietnam	Vietnam	Embedded Software Performance Optimization based on Generating the Simulation Code of Functions
11:30	11:50	20	20	Oral	Trong-Thuc Hoang*, Ngoc-Hung Nguyen*, Xuan-Thuan Nguyen*, Trong-Tu Bui*	The University of Science, Ho Chi Minh City, Vietnam	Vietnam	A Real-Time Object-Recognition System based on PCNN Algorithm
11:50	13:00	70		Lunch				

Session #10		Location: Song Han room			Chairs: Koichiro Ishibashi (The University of Electro-Communications) Nguyen Van Cuong (Danang University of Technology)			
13:00	13:20	20	15	Oral	Lam Duc Khai*, Huynh Huu Thuan**, Vu Duc Lung*	*University of Information Technology, VNU-HCM, Vietnam **University of Science, VNU-HCM, Vietnam	Vietnam	An Autonomous Navigation System Based on Hierarchical Implicit Shape Modeling (HISM) - Hardware Algorithm Compatible with FPGA
13:20	13:40	20	34	Oral	T. T. Hoang, D. A. Viet, N. T. T. Van, P. D. Tuan, T. Q. Vinh	VNU University of Engineering and Technology, Vietnam	Vietnam	Extended Kalman Filter in Mobile Robot and FPGA-based Implementation
13:40	14:00	20	23	Oral	Vu Duc Tiep, Nguyen Van Ninh, Le Van Bien, Truong Vu Bang Giang	VNU University of Engineering and Technology, Vietnam	Vietnam	A Dual-band Microstrip Antenna for Mobile Handsets
14:00	14:20	20	35	Oral	T.T. Hoang, P.M. Duong, N.V. Tinh, T.Q. Vinh	VNU University of Engineering and Technology, Vietnam	Vietnam	A Path Following Algorithm for Wheeled Mobile Robot Using Extended Kalman Filter
14:20	14:40	20	8	Oral	Long Hoang Duc, Duong Bach Gia	VNU University of Engineering and Technology, Vietnam	Vietnam	Study, Design and Fabrication of Low Noise Amplifier to be used for the New-Generation Surveillance Radar Operating at UHF Band
14:40	15:00	20		Closing	- Prof. NGUYEN Van Tuan* - Prof. Toshimasa MATSUOKA** - Dr. BUI Trong Tu***	*DUT, Vietnam **Osaka University, Japan ***HCM University of Science		- Conference summary - Closing speech - ICDV 2013 announcement