

PROGRAM

The 2024 9th International Conference on Integrated Circuits, Design and Verification (ICDV 2024)

Venue: VNU Information Technology Institute, No. 144, Xuan Thuy Road, Cau Giay District, Hanoi, Vietnam

Day 1: Thursday, 6 June 2024	
8:30 AM - 10:00 AM	<p style="text-align: center;">Plenary session</p> <p>Keynote #1: Restart of Japan Semiconductor and Our Activity on Steep Slope devices and Cryo-CMOS for Quantum Computing Jiro Ida, Kanazawa Institute of Technology, Japan</p> <p>Industry Invited Talk: Survey of ML Applications in Function Verification Mr. Michael Chiang, Deputy Director, Siemens EDA Venue: Jonathan KS Choi Center, Vietnam National University Hanoi Chairs: Prof. Xuan-Tu Tran (VNU Information Technology Institute, Vietnam)</p>
10:00 AM	Tea break
10:15 AM- 12:25 PM	<p style="text-align: center;">Session: AI Circuits & Systems</p> <p>Keynote #2: Survey and Exploration of Advancements in Hardware Neural Computing Oscal Tzyh-Chiang Chen, National Chung Cheng University, Taiwan Venue: Jonathan KS Choi Center, Vietnam National University Hanoi Chairs: Prof. Cong-Kha Pham (University of Electro-Communications, Japan)</p>
11:15AM- 12:30PM	<p style="text-align: center;">Session: Special session on Signal Processing for Manufacturing Applications</p> <p>Venue: Room 603, Building E3, Vietnam National University Hanoi Chairs: Dr. Dinh-Thai Kim, Dr. Nhu-Tung Ngyen (VNU International School, Vietnam)</p>
12:30 PM	Lunch
1:30 PM - 3:10 PM	<p style="text-align: center;">Keynotes #3: Hardware accelerator design optimization for PQC algorithms Makoto Ikeda, University of Tokyo, Japan</p> <p>Keynote #4 Secured Network-on-Chip (SoC) Framework for RISC-V Computer System Trong-Thuc Hoang, Binh Kieu-Do-Nguyen, and Cong-Kha Pham, University of Electro-Communications (UEC), Tokyo, Japan Venue: Jonathan KS Choi Center, Vietnam National University Hanoi Chairs: Prof. Van-Phuc Hoang (LQDTU, Vietnam)</p>
3:10 PM	Tea break
3:25 PM - 5:10 PM	<p style="text-align: center;">Session: HW security & cryptography</p> <p>Venue: Jonathan KS Choi Center, Vietnam National University Hanoi Chairs: Prof. Quang-Kien Trinh (LQDTU, Vietnam)</p>
3:25 PM - 5:10 PM	<p style="text-align: center;">Session: Special Session on Intelligent Control & AI I</p> <p>Venue: Room 603, Building E3, Vietnam National University Hanoi Chairs: Dr. Manh-Hung Ha, Dr. Nhu-Tung Nguyen (VNU International School, Vietnam)</p>
5:30 PM	<p>Banquet (a shuttle bus will pick you up at the conference venue) Location: Sheraton Hanoi West Address: 36 Le Duc Tho Road, Nam Tu Liem District, Hanoi, Vietnam</p>

Day 2: Friday, 7 June 2024	
8:30 AM - 9:30 AM	<p style="text-align: center;">Keynote #5 Online ASR and ICA for EEG Signal Processing: Algorithm, Architecture, and Implementation</p> <p style="text-align: center;">Lan-Da Van, National Yang Ming Chiao Tung University, Taiwan Venue: Jonathan KS Choi Center, Vietnam National University Hanoi Chairs: Prof. Thanh-Loan Pham-Nguyen (Hanoi University of Science & Technology, Vietnam)</p>
9:30 AM - 10:40 AM	<p style="text-align: center;">Session: SoCs & Systems</p> <p style="text-align: center;">Venue: Jonathan KS Choi Center, Vietnam National University Hanoi Chairs: Prof. Thanh-Loan Pham-Nguyen (Hanoi University of Science & Technology, Vietnam)</p>
10:55AM - 11:55AM	<p style="text-align: center;">Session: Verification & DFT</p> <p style="text-align: center;">Venue: Jonathan KS Choi Center, Vietnam National University Hanoi Chairs: Prof. Koichiro Ishibashi (University of Electro-Communications, Japan)</p>
10:55AM-11:40 AM	<p style="text-align: center;">Online Session</p> <p style="text-align: center;">Venue: Room 603, E3, Vietnam National University Hanoi Zoom Link: https://us06web.zoom.us/j/6218753030?pwd=kjvwI0ZbVTuUmcuF4YaObUXtcaZRKu.1&omn=87973376932 Chairs: Prof. Khanh N. Dang (University of Aizu, Japan)</p>
11:55 AM	Lunch
1:30 PM - 2:30 PM	<p style="text-align: center;">Keynote #6 Towards battery-free and low-cost distributed sensor node: novel IC approaches</p> <p style="text-align: center;">Orazio Aiello, University of Genova, Italy Venue: Jonathan KS Choi Center, Vietnam National University Hanoi Chairs: Dr. Van-Trung Nguyen (LQDTU, Vietnam), Prof. Thanh-Toan Dao (University of Transportation and Communications, Vietnam)</p>
2:30 PM - 4:10 PM	<p style="text-align: center;">Session: RF, sensors & interfaces</p> <p style="text-align: center;">Venue: Jonathan KS Choi Center, Vietnam National University Hanoi Chairs: Dr. Van-Trung Nguyen (LQDTU, Vietnam), Prof. Thanh-Toan Dao (University of Transportation and Communications, Vietnam)</p>
2:15 PM - 4:10 PM	<p style="text-align: center;">Session: Special Session on Intelligent Control & AI II</p> <p style="text-align: center;">Venue: Room 603, Building E3, Vietnam National University Hanoi Chairs: Dr. Manh-Hung Ha (VNU International School, Vietnam), Dr. Le-Cuong Vo (Hanoi University of Science & Technology, Vietnam)</p>
4:10 PM	Tea break
4:25 PM - 5:10 PM	<p style="text-align: center;">Session: Analog & Mixed signal circuits</p> <p style="text-align: center;">Venue: Jonathan KS Choi Center, Vietnam National University Hanoi Chairs: Dr. Orazio Aiello (Univesity of Genova, Italy)</p>
5:10 PM	Closing session
Day 3: Saturday, 8 June 2024	
<p style="text-align: center;">Social Event: A day trip to HaLong Bay (7:00 AM - 8:00 PM) Fee: \$99/person (registered separately at the conference desk)</p>	