



# CALL FOR PAPERS

The 11<sup>th</sup> International Conference on Integrated Circuits, Design, and Verification (ICDV 2026)  
October 08-10, 2026 – Hanoi, Vietnam

<http://icdv-conf.org>

*Organized by IEEE CASS & SSCS Vietnam Chapter;  
co-Hosted by*

*Le Quy Don Technical University & VNU Information Technology Institute*

The International Conference on Integrated Circuits, Design, and Verification (ICDV) provides a forum for exchanging ideas, discussing research results, and presenting chips, circuit designs and applications in solid-state and semiconductor fields. Continuous scaling of the CMOS devices increases the number of transistors on a VLSI chip. It reaches the level of 100 giga transistors on a single chip, which is comparatively higher than the total neuron numbers in the human brain. This would certainly provide a great opportunity for new applications and information processing. On the other hand, the small feature size causes new problems, such as leakage current and process variation. To discuss utilizing the scaling advantages and coping with the new problems, we call for contributions about new proposal of application systems, VLSI architectures, and design methodologies as well as the technologies and applications in the integrated circuits and devices field. We expect this conference to explore and stimulate the contributed research on those subjects. The papers are solicited from prospective authors interested in the related fields.

The ICDV 2026 conference is organized by IEEE CASS & SSCS Vietnam Chapter, co-hosted by Le Quy Don Technical University & VNU Information Technology Institute (VNU-ITI), technically supported by the IEICE Vietnam Section. The ICDV 2026 will be held in Hanoi, Vietnam. Further information about the conference, paper submission guidelines and templates will be updated on the website.

## Paper Submission

Prospective authors are invited to submit full-length, six-page manuscripts, including figures, tables and references, to the official ICDV 2026 website. All papers will be handled electronically. Accepted and presented papers will be included in the conference proceedings with ISBN and **will be submitted for inclusion in IEEE Xplore®** (the copyright shall be assigned to the IEEE). The proceedings of ICDV series are regularly indexed by Scopus and listed in the conference proceeding citation index (CPCI) of Clarivate. Papers are solicited in the following categories but are not limited to:

- |  |   |
|--|---|
| 1. Digital integrated circuits and signal processing   | 10. Embedded systems design                             |
| 2. Memory and processors/multiprocessors design        | 11. AI & IoT related circuits, devices and applications |
| 3. Analog and mixed-signal circuits                    | 12. Rebooting computing                                 |
| 4. RF integrated circuits and microwave engineering    | 13. Hardware security, cryptography                     |
| 5. Advanced packaging and semiconductor design         | 14. Industrial IoT devices and systems.                 |
| 6. Photonics devices, circuits and systems             |   |
| 7. Circuit and advanced design technologies            | <b>Special sessions:</b>                                |
| 8. Circuits/devices modeling, verification and testing | ▪ <i>Quantum computing and quantum AI</i>               |
| 9. Reconfigurable architectures & FPGA-based designs   | ▪ <i>TBD</i>  |

## Important dates

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|-------------------------------|---------------------|---|
| ▪ Full manuscript submission: | <b>May 30, 2026</b> | <b>Submission link:</b> <a href="https://edas.info/N35217">https://edas.info/N35217</a> |
| ▪ Notification of acceptance: | July 20, 2026       |   |
| ▪ Camera-ready submission:    | August 20, 2026     |   |

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